

Name of Teaching Staff : Prof. V. Venkataramanan.
Designation : Assistant Professor
Department : Electronics & Telecommunication Engineering
Date of Joining the Institution : 4.1.2016



Qualifications with Class / Grade : 1. M.Tech., (VLSI Design), S.R.M University, First Class CGPA: 7.94, 2010.
2. M.Sc., (Electronics and Communication), Integrated P.G Course, Annamalai University 1st Class with Distinction, CGPA: 9.43, 2008. (Mumbai University Equivalence is obtained)

Total Experience in Years : **Teaching: 7 years 9 months**
1. Assistant Professor (Adhoc) in D.J. Sanghvi College of Engg. from Jan, 2016 till date.
2. Assistant Professor in Arunai College of Engg. from 16.12.2010 to 31.10.2015.

Papers Published : **National/International: 28**
1. A. Manikandan, V. Venkataramanan, M. Kavitha and S.Parvathi, "Performance Analysis of LTE Physical Layer Based on Release 8&9 through Simulink Environment", *International Journal of Advanced Technology & Engineering Research (IJATER)*, pp. 92 – 97, Nov. 2012.
2. K.S Ramesh and V. Venkataramanan, "Switching Activity Reduction Using Scan Shift Operation", *Journal of Computer Applications* pp. 409 – 419, February 10, 2012.
3. M. Kavitha and V. Venkataramanan, "Performance Analysis of LTE Physical Layer Based on Release 10", *CiiT International Journal on Wireless Communication*, pp. 197 – 201, 2013.
4. S. Archana V. Venkataramanan, K. Shankar and D. Vinoth, "Analysis of Cluster Based Zone Routing Protocol in MANET through Network Simulator", *International Journal of Engineering and Advanced Technology*, pp. 50 – 53, 2013.
5. Sasikiran.C and V. Venkataramanan, "FPGA Design and Implementation of Different Modulation Schemes Using Xilinx System Generator", *International Journal of Applied Engineering Research*. June. 2015.
6. A. Manikandan, V. Venkataramanan and R. Janani, "Software Defined Radio Implementation in 3GPP Systems", *ARPJN Journal of Engineering and Applied Sciences*, July 2015.
7. Sasikiran.C and V. Venkataramanan, "Model-Based Design of LTE Baseband Processor Using Xilinx System Generator in FPGA." *ARPJN Journal of Engineering and Applied Sciences*, August 2015.

Papers Published

8. S. Abirami, A. Manikandan and V. Venkataramanan, "Interference Cancellation in Multiple Input Multiple Output Cognitive Radio Networks", *International Journal of Advanced Technology & Engineering Research (IJATER)*, Nov 2015.
9. Megh Shukla, and V. Venkataramanan "Road Safety Model", *Technofocus*, Mar 2016.
10. Shashank Rane, Ravi Maurya, Vaibhav Zaveri, Deepak Rayani and V. Venkataramanan "Vehicle Movement Detection Using Street Lights", *Technofocus*, Mar 2016.
11. Bhaskar Kulkarni, Parth Joshi, Yash Agrawal, and V Venkataramanan, "Security Enabled Wireless LED Display for Notice board", *Technofocus*, Oct 2016.
12. Rachita Shah, Udit Shah, Rishit Turakhia, Venkataramanan. V, "Electronic Voting Machine", *Technofocus*, Mar 2017.
13. Aditi Patel, Mital Pattani, Abhishek Satpute, Venkataramanan. V "Automated AC Dimmer", *Technofocus*, Mar 2017.
14. Venkataramanan. V,V.Kannan, "Bit Error Rate Analysis of LTE Physical Layer using Different Channel Model", *International Journal on Communications Antenna and Propagation (Scopus Indexed)*, Vol.7, pp-641-647, 2017.
15. Venkataramanan. V, S.Lakshmi, "A Case Study of Various Wireless Network Simulation Tools", *International Journal of Communication Networks and Information Security (Scopus Indexed)*, Vol.10, pp-389-396, 2018.
16. Aditi Patel, Abhishek Satpute, Mital Pattani, V Venkataramanan, "Virtual Piano", *Proceedings of International Conference on Wireless Communication*, pp 265-272, 2018.
17. Jeet D. Sanghavi, Alay M. Shah, Saurabh S. Rane, V. Venkataramanan, "Smart Traffic Density Management System Using Image Processing" *Proceedings of International Conference on Wireless Communication*, pp 301-312, 2018.
18. Venkataramanan. V and T.Ramya, "Matlab Implementation of Channel Estimation and Modulation Technique for MIMO", *Proceedings of NCRVIC- 2010*, B.S. Abdur Rahman University, March 31, 2010.
19. V. Venkataramanan, "FPGA Design and Implementation Channel Estimation and Modulation Technique for MIMO", *National conference on Emerging Trends in Micro/Nano Electronic Technology*, Saveetha University, 8th April 2011.
20. K.S Ramesh and V. Venkataramanan "Switching activity reduction using Scan Shift operation", *National conference on computational intelligence in Electrical and Electronics Engineering*, Sathyabama University, 1st and 2nd March 2012.
21. Kavitha. M and Venkataramanan. V, "Performance Analysis of Modulation Technique for MIMO", *National Conference on Information Technology (NCIT – 2012)*, Pondicherry Engineering College, 2012.
22. Kavitha. M and Venkataramanan. V, "Performance Analysis of LTE Physical Layer Based on Release 10", *National Conference on Advanced computing & Information Technology NACIT-13*, Jerusalem Engineering College, May 2013.
23. Sharmili. R, and Venkataramanan. V, "An Integrated Automatic Test Pattern Generation And Implementation Using Device Under Test (DUT)", *National Conference on Communication system and VLSI Design (NCCSVD-14)*, PSG College of Technology, Jan 2014.

24. Sasikiran.C and Venkataramanan. V, “An Improved Design of Carry Skip (BYPASS) Adder Using Reversible Logic”, *National Conference on Information & Communication Technologies (NCICT 2014)*, SSN College of Engineering, April 2014.
25. Sasikiran.C and Venkataramanan. V, “Model Base Design of Modulation Techniques Using Xilinx System Generator in FPGA” *National Conference on Microelectronics, Networking and Communication Technologies (NCMNC 2015)*, SRM University, April 2015.
26. Akramsherkhan, Karthik.P, Sudhanthiram.P and Venkataramanan. V, “Versatile Self Navigating Robot base for Agricultural Applications” *National Conference on Reliability and Safety Engineering (NCRS-2016)*, SSN College of Engineering, Dec 2016.
27. K.S Ramesh and V. Venkataramanan, “Fault Coverage and Switching Activity Reduction Using Scan Shift Operation”, *International Conference on Computer Vision (ICCV-2012) Singapore*.
28. V. Venkataramanan, “Performance Analysis of Digital Circuit Design under different FPGA Families” *National Conference on Recent Trends in Electronics and Communication (NCRTECE-15)*, SMK Forma Institute of Technology, March 2015.

Papers Presented in Conferences	:	13	
PhD Guide? Give field & University	:	Field:	--
		University:	--
PhDs / Projects Guided	:	PhDs :	--
		Projects at Masters level:	6
Books Published / IPRs / Patents	:		--
Professional Memberships	:	ISTE,IEEE,IETE	
Consultancy Activities	:		--
Awards	:	First Rank in Integrated PG Degree Course	
Grants fetched	:		--
Interaction with Professional Institutions	:	Invited as Resource Person, Session Chair in Various Conferences, Workshop and Review Member in Journals	