Name of Teaching Staff	:	Prof. Mrunalini Pimpale			
Designation	:	Assistant Professor			
Department	:	Electronics & Telecommunication Engineering			
Date of Joining the Institution	:	04.7.2013  Mrunalini.ingle@djsce.ac.in			
Email ID	:				
Office Contact	:	022-42331214			
Google Scholar Link	:	https://scholar.google.com/citations?user=CUvPuEEAAA AJ&hl=en			
Research gate Link:		https://www.researchgate.net/profile/Ameya-Kadam-3			
Qualifications with Class / Grade	:	<ol> <li>M.Tech. – Electronics Engineering from Yashwantrao Chavan College of Engineering, Nagpur Autonomous in June, 2012, 1st class with CGPA 7.59</li> <li>B.E. (Electronics Engineering) from Rashtrasant Tukadoji Maharaj Nagpur University in June 2008, 1st class with 71.6%.</li> </ol>			
Total Experience in Years	:	<ol> <li>Teaching: 12 years</li> <li>Assistant Professor, D.J. Sanghvi College of Engineering (Electronics and Telecommunication Engineering Department) from 4.7.2013 to till date.</li> <li>Assistant Professor, D.J. Sanghvi College of Engineering (Engineering Department) from 11.7.2012 to 30.6.2013 on Adhoc basis.</li> <li>Lecturer, Yashwantrao Chavan College of Engineering Nagpur, from 11.08.2009 to 30.04.2012.</li> </ol>			
Papers Presented in Conferences	:	<ul> <li>International: 3</li> <li>[1] Rohan Vaidya, Darshan Trivedi, Sagar Satra, Mrunalini Pimpale "A Novel Approach Towards Handwritten Character Recognition Using Deep Learning" Proceeding of 2018 Second International Conference on Inventive Communication and Computational Technologies (ICICCT), IEEE, 2018 ,pp 772-775. April 2018</li> <li>[2] Mrunalini Pimpale, Tejaswini Panse, "Radix-10 Parallel Decimal Multiplier" Proceedings of International Conference on Electronics and Communication Engineering, January 2012</li> <li>[3] Mrunalini Pimpale, Tejaswini Panse," Fixed –Point Parallel Decimal Multiplier" ICEECS-2012, May 2012</li> </ul>			
Area of Specialization		Analog VLSI, Analog Signal Processing, Embedded Systems.			
Professional Memberships	:	Life Member of Indian Society of Technical Education (ISTE)			

Subjects Taught	UG Level: [1] Digital Communication
	[2] Analog Electronics –I
	[3] Analog Electronics –II
	[4] Analog circuit Design
	[5] Integrated Circuits
	[6] Electronics Devices and circuits-I
	[7] Electronics Devices and circuits-II
	[8] VLSI Design
	[9] Microprocessor and Microcontroller
	[10] Digital System Design
	[11] Electronics and Measurement Instrumentation
D : G : 1 1	
Projects Guided	: UG Level: (Last 4 years) [1] Object Detecting Autonomous Water Surface Cleaning Bot.
	[2] Sketch recognition using Siamese network.
	[3] Smart Visual Local Navigation AID for the Visually Impaired.
	[4] Metal Detector Robotic Vehicle.
	[5] Closed Loop BLDC motor for Electric cars.
	[6] Wireless Rover with Robotic Arm and Visual Assistance.
	[7] Sleep detector using EEG signal.
	[8] Gesture and Voice control Mouse.
	[9] SEO optimizer and suggester.
	[10] Handwriting detection and recognition using Neural Network.
	[11] Smart trolley using RFID.
	[12] Analysis of 5G network.

Recommended Students	Name of the Student	University/Industry							
for Higher Education	Aniket Rao	University of Michigan Rackham							
		Graduate School							
	Shraddha Sutar	University of Illinois							
	Ayush Shah	• Georgia Tech Graduate Admissions							
	Vidhi Raval	University of Colorado Boulder							
	Sagar Samant	<ul> <li>Texas University</li> </ul>							
	Nikit Shah	• Clemson University (Graduate)							
	Chirag Shah	University of Pennsylvania							
	Vanthana Sachdev	University of Pennsylvania							
	Mitali Salvi	<ul> <li>Georgia Tech Graduate</li> <li>Admissions</li> </ul>							
	Priyal Raut	<ul> <li>University of Pennsylvania</li> </ul>							
	Raj Visharia	Georgia Tech Graduate     Admissions							
	Rohan Deo	University of Sadan California							
	Harsh Raval	Oxford University							
	Prakhar Sah	• Virginia Polytechnic Institute & State University							
	Riddhi Thakkar	Worcester Polytechnic Institute							
Institute/Department	> NAAC Criteria 5								
Responsibility handled:	> NBA Criteria 8								
	➤ Electronics Design/ EHW & simulation Lab In-charge								
	➤ Admission Committee Member								
	> SE Class Teacher.								
	Academic Bulletin (half yearly news editor)								
	Department Website Coordinator.								
	➤ Part of organizing committee of International conference (ICWiCOM-								
	<ul> <li>2021).</li> <li>Organized STTP on "Recent Trends in wireless networks"</li> <li>Organized Webinar on 'Recent Trend in Microwave and Internet of</li> </ul>								
								Things'.	