



REGULAR EXAMINATION TIMETABLE, JANUARY, 2024
BACHELOR OF TECHNOLOGY (B.Tech.)
SECOND YEAR, SEMESTER-III (For 2023-24 Batch Under Autonomy)
Timings: 2:30 p.m. to 4:30 p.m.

Date: 2nd December, 2023

| Day & Date | Mechanical Engineering | Electronics and Telecommunication Engg. | Information Technology | Computer Engineering | Computer Science & Engineering (Data Science) | Artificial Intelligence and Data Science | Artificial Intelligence and Machine Learning | Computer Science and Engineering (IOT and Cyber Security with Block Chain Technology) |
|-----------------------|-----------------------------|---|--|-----------------------------|---|--|--|---|
| Friday, 05/01/2024 | Universal Human Values | Universal Human Values | Logic Design and Computer Architecture | Digital Electronics | Mathematics for Intelligent Systems | Universal Human Values | Universal Human Values | Universal Human Values |
| Monday, 08/01/2024 | Engineering Thermodynamics | Electronics Circuit Design | Data Structures | Data Structures | Data Structures | Data Structures | Data Structures | Data Structures |
| Wednesday, 10/01/2024 | Mechanics of Materials | Digital System Design | Database Management System | Database Management Systems | Database Management System | Database Management Systems | Database Management Systems | Database Management Systems |
| Friday, 12/01/2024 | Manufacturing Processes | Signals and Systems | Operating System | Discrete Structures | System Fundamentals | Operating Systems | Operating Systems | Operating Systems |
| Monday, 15/01/2024 | Engineering Mathematics-III | Engineering Mathematics-III | Discrete Structures | Engineering Mathematics-III | - | Engineering Mathematics-III | Engineering Mathematics-III | Engineering Mathematics-III |

Important Note: Changes if any, in the time table, shall be communicated on the college website (www.djsce.ac.in)

Ms. Alka Shukla
Controller of Examinations

Dr. Hari Vasudevan
Principal