



Shri Vile Parle Kelvani Mandal's
**Dwarkadas J. Sanghvi
 College of Engineering**



Approved by AICTE, New Delhi and Affiliated to the University of Mumbai

Department of Electronics Engineering
 Time Table for the academic term : July – Nov 2012

W.E.F. 23/7/2012

S.E. ELEX / SEM III

CLASS NO : 33

No.	Period	Monday				Tuesday				Wednesday				Thursday				Friday				Saturday
		CS (RN)	CS (BS)	BEC (SAK)	PCT (SR)	EN (SP)	BEC (SAK)	CS (BS)	PCT (SR)	EN (SP)	BEC (SAK)	CS (BS)	PCT (SR)	EN (SP)	BEC (SAK)	CS (BS)	PCT (SR)	EN (SP)	DSD (MI)	CS (SPK)	EN (SP)	
1	08:30-09:30	Maths (RN)				CS (BS)				BEC (SAK)	CS (BS)	PCT (SR)	EN (SP)	Maths (RN)				PCT (AS)	EN (SP)	DSD (MI)	CS (SPK)	EN (SP)
2	09:30-10:30	CS (BS)				DSD- I (CB)				(B1)	(B2)	(B3)	(B4)	BEC (SAK)				(B1)	(B2)	(B3)	(B4)	Maths (RN)
3	10:30-10:45																					
4	10:45-11:45	CS (BS)	DSD (CB)	BEC (SAK)	PCT (SR)	BEC (SAK)				DSD- I (CB)				EN (SP)	BEC (KM)	CS (BS)	DSD (CB)	BEC (SAK)				DSD- I (CB)
5	11:45-12:45	(B1)	(B2)	(B3)	(B4)	EN (SP)				Maths (RN)				(B1)	(B2)	(B3)	(B4)	CS (BS)				PCT (SR)
6	12:45-01:30																					
7	01:30-02:30	EN (SP)				DSD (CB)	PCT (SR)	EN (SP)	BEC (KM)	CS (BS)				EN (SP)				DSD- I (CB)				
8	02:30-03:30	PCT (SR)				(B1)	(B2)	(B3)	(B4)	BEC (SAK)								Maths (TUT) (RN)				
9	03:30-04:30																					
10	04:30-05:30																					

HOD (ELEX Engg.)

CONVENOR (TIMETABLE COMMITTEE)

PRINCIPAL



Shri Vile Parle Kelvani Mandal's
**Dwarkadas J. Sanghvi
 College of Engineering**



Approved by AICTE, New Delhi and Affiliated to the University of Mumbai

Department of Electronics Engineering
 Time Table for the academic term : July – Nov 2012

W.E.F. 23/7/2012

T.E. ELEX / SEM V

CLASS NO : 34

No.	Period	Monday	Tuesday	Wednesday	Thursday	Friday	Saturday						
1	08:30-09:30	LICD (MI)	CTSS (AM)	LICD (MI)	μPμC (KM)	DCCT (SK)	EVS (VB)	CTSS (RS)		DCCT (RY)	μPμC (KM)	WS (DS)	WS (VP)
2	09:30-10:30	EVS (VB)	(B1)	(B2)	(B3)	(B4)	LICD (MI)	EE (BS)	EE (TUT) (B3+B4)	(B1)	(B2)	(B3)	(B4)
3	10:30-10:45												
4	10:45-11:45	CTSS (RS) (33)	EE (BS)	WS (DS)	WS (VP)	LICD (MI)	CTSS (RS)	SOFT SKILLS (33)	LICD (MI)	CTSS (AM)	DCCT (SK)	μPμC (KM)	EE (BS)
5	11:45-12:45	DCCT (SK) (33)	CTSS (RS)	(B1)	(B2)	(B3)	(B4)	DCCT (SK) (33)	(B1)	(B2)	(B3)	(B4)	LICD (MI)
6	12:45-01:30												
7	01:30-02:30	WS (DS)	WS (VP)	CTSS (RS)	LICD (MI)	LICD (MI) (33)	CTSS (RS)	μPμC (KM)	DCCT (RY)	WS (DS)	WS (VP)	DCCT (SK)	μPμC (KM) (33)
8	02:30-03:30	(B1)	(B2)	(B3)	(B4)	EE (TUT) (B1+B2) (33)	DCCT (SK)	(B1)	(B2)	(B3)	(B4)	EVS (TUT) (VB)	μPμC (KM) (33)
9	03:30-04:30											μPμC (KM)	
10	04:30-05:30											μPμC (KM)	

HOD (ELEX Engg.)

CONVENOR (TIMETABLE COMMITTEE)

PRINCIPAL



Shri Vile Parle Kelvani Mandal's
**Dwarkadas J. Sanghvi
 College of Engineering**



Approved by AICTE, New Delhi and Affiliated to the University of Mumbai

Department of Electronics Engineering
 Time Table for the academic term : July – Nov 2012

W.E.F. 23/7/2012

B.E. ELEX / SEM VII

CLASS NO : 34

No.	Period	Monday			Tuesday				Wednesday	Thursday				Friday				Saturday			
1	08:30-09:30	DCN (MP)	VLSI (SP)	IP (SK)	PED (MP)				P	VLSI (SP)	FD (PSJ)	IP (SK)	DCN (MP)	DCN (CB)				IP (SAK)			
2	09:30-10:30	(B2)	(B3)	(B4)	FD (PSJ)				R	(B1)	(B2)	(B3)	(B4)	VLSI (DS)				PED (MP)			
3	10:30-10:45								O												
4	10:45-11:45	VLSI (DS)			FD (PSJ)	PED (MP)	DCN (CB)	VLSI (DS)	J	VLSI (DS)				FD (PSJ)				DCN (MP)	IP (SAK)	FD (PSJ)	PED (SB)
5	11:45-12:45	PED (MP)			(B1)	(B2)	(B3)	(B4)	E	IP (SAK)				FD (PSJ)				(B1)	(B2)	(B3)	(B4)
6	12:45-01:30								C												
7	01:30-02:30	IP (SAK)			VLSI (DS)				T	DCN (CB)				IP (SAK)	VLSI (DS)		FD (PSJ)	FD (PSJ)			
8	02:30-03:30	DCN (CB)			IP (SAK)				D	PED (MP)				(B1)	(B2)		(B4)	DCN (CB)			
9	03:30-04:30	PED (PR) (SB)							A	PED (PR) (SB)											
10	04:30-05:30	(B1)							Y	(B3)											

HOD (ELEX Engg.)

CONVENOR (TIMETABLE COMMITTEE)

PRINCIPAL