

Faculty Profile



- Name of Teaching Staff** : Vivek Vijay Nar
- Designation** : Assistant Professor
- Department** : Electronics Engineering
- Date of joining the Institution** : 23rd January 2015.
- Qualifications with Class/ Grade** : M.Tech in Electronics and Telecommunication, First Class (CGPA-9.2), VJTI, Mumbai, July 2013.
B.Tech in Electronics, First Class (CGPA-7), VJTI, Mumbai, May 2011.
Diploma in Electronics, First Class with Distinction (93.58%), VJTI, Mumbai, May 2008.
- Total Experience in years** : Teaching 1yr
Assistant Professor (Ad-hoc) in Electronics, D. J. Sanghavi College of Engineering from 23/1/15 till date.
Assistant Professor (Ad-hoc) in Electronics and Telecommunication, St. John College of Engineering and Technology, Palghar from 16/6/14 to 22/1/15.
Industry – 8 months
Telecomm Design Engineer in DAR Consultancy, Pune from 5/8/13to 21/3/14.
- Papers Published** : International Journal -Two Papers
1) Vivek Vijay Nar, Alice N. Cheeran: Effective Implementation of Static Voice Alteration, International Journal of Computer Applications.
2) Vivek Vijay Nar, Alice N. Cheeran, Souvik Banerjee: Verification of TD-PSOLA for Implementing Voice Modification, International Journal of Engineering Research and Applications, ISSN: 2248-9622, Vol. 3, Issue 3, May-Jun 2013, pp.

Awards

: Recipient of 'Lord Reay Gold Medal' for Highest Aggregate Marks in the Final Year Diploma Examination in May 2008.

Practical Training

- Worked as Inplant Trainee in EBG-MCCB of Larsen And Toubro Limited;
- Attended Summer Training Internship Program'2010 in "Embedded System" at ThinkLab, Mumbai.
- Attended "6th INUP Familiarization Workshop on Nanofabrication Technologies" held at IIT Bombay Mumbai.
- Attended National Instruments India Professional Training on "LabVIEW Core 1 & Core 2".
- Attended "INUP Hands-on Training Workshop on Fabrication & Characterization of Crystalline Silicon Solar Cells" held at IIT Bombay Mumbai.
- Attended a two day workshop on "Embedded System" held at Mukesh Patel School of Technology Management and Engineering, Mumbai.
- Attended a two day faculty development workshop on "Embedded System Design using ARM mbed & Cypress PSoC" held at SPIT, Mumbai.
- Attended AICTE Funded two week faculty development program on "Electronic System Design: From Devices to Applications" held at SPIT, Mumbai.
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