

**Name:** Prasad S. Joshi  
Associate Professor & Head  
Department of Electronics Eng.



**Address:** D/204, Radha Govind,  
Opp. Dheeraj Savera,  
Nr. Siddharth Nagar,  
Off. W. E. Highway,  
Borivali (east),  
Mumbai 400066

**Email:** prasad.joshi@djsce.ac.in,  
prasad.joshee@gmail.com

**Phone:** (O): 022-26107010, 26107461 - ext. 5020  
Direct - 42335020  
(Cell): 09833314377

**Date of Birth:** 16<sup>th</sup> October 1970

**Academic Qualification:**

| Degree                             | Month/<br>Year | College/<br>University                            | Marks/<br>Class                |
|------------------------------------|----------------|---|--------------------------------|
| B.E.(Electronics)                  | May 1992       | M. H. Saboo<br>Siddik COE<br>Mumbai<br>University | 62 % / First<br>Class (Honors) |
| M. E.<br>(Electronics)             | May 2000       | V. J. T. I.<br>Mumbai<br>University               | 69 % / First<br>Class (Honors) |
| Ph.D. (Tech.)<br>(Electronics Eng) | October 2016   | V. J. T. I.<br>Mumbai<br>University               | -                              |

**Experience:**

| Period                        | Organization                              | Position   | Job Profile  |
|-------------------------------|---|--|--|
| August 2010 –<br>Till date    | D. J. Sanghvi<br>COE<br>Vile Parle (west) | Associate<br>Professor &<br>Head Dept.<br>of Electronics | Manage and<br>Monitor dept.<br>activities on a day-<br>to-day basis. Plan<br>for projects and<br>help foster<br>industry<br>interaction. |
| October 2009 –<br>August 2010 | D. J. Sanghvi<br>COE<br>Vile Parle (west) | Head Dept.<br>of Electronics                             |  |

|                               |  |  |  |
|-------------------------------|--|--|--|
| August 2001 -<br>October 2009 | D. J. Sanghvi<br>COE<br>Vile Parle (west)  | Assistant<br>Professor<br>(Dept of<br>Electronics) | Teaching - TE, BE<br>( $\mu$ p/ $\mu$ c, Filter<br>Theory, Advance<br>DSP, Robotics)<br>Handling<br>departmental<br>activities.<br>Guiding Projects. |
| August 2001 -<br>Sept. 1995   | D. J. Sanghvi<br>COE<br>Vile Parle (west)  | Lecturer<br>(Dept of<br>Electronics)               | Teaching - FE, SE<br>(BEE, C Prog.<br>Electrical Net,<br>PCOM)<br>Guiding Projects.  |
| Sept. 1995 -<br>Sept. 1993    | Shri Bhagubhai<br>Mafatlal<br>Polytechnic. | Lecturer<br>(Dept. of IE)                          | Teaching -<br>Diploma Students.<br>Development of<br>PCB lab.  |
| Sept. 1993 -<br>April 1993    | X'pro Computers<br>(Wipro -<br>Franchisee) | Service Eng.                                       | Servicing<br>Desktops.<br>Maintaining LAN<br>at SHCIL  |

### **Projects Handling/Association:**

- Handling BE level Projects (in-house) since 1997.
- ME project: ECG Signal Analysis Using Wavelet Transforms.  
Tools: ECG signal simulator, Lab Windows CVI environment.
- Conducted CEP (Continuing Education Program) for college students in C, C++, VB and Java with fellow faculty in the period 1999 – 2001. Program had a successful run for three consecutive semesters, when the topics were introduced in the regular academic syllabus.
- Contributed concretely to the design development and implementation of 89C52 based 'Design Development Module'. Presently the same being used for conducting practicals in the subject of Microprocessors and Microcontrollers.
- Played a pivotal role in organizing an ISTE recognized Short Term Training Programme on 'Microcontrollers and Embedded Systems Design' conducted in the college between the period 27 November 2006 – 7 December 2006. Programme attended by 42 faculties from engineering colleges in Maharashtra.

- Designed and developed ‘Temperature and Humidity Indicator’ based on Atmel ATmega8L, for ‘Saguna Baug’, a farm located in Neral.
- Rendering consultancy based services to ‘Deep Electronics’, Vasai (an SSI involved in industrial product development), for developing a range of ‘Analytical Lab Grade’ equipments.
- Reasonable working and development experience in x51 and AVR core, as well as corresponding C cross compilers (Keil and GCC) and debug, development simulation environment.
- Chief contributor in the conduction of a 15 – day workshop for engineering college teachers held at Mukesh Patel School of Technology and Management Engineering, Juhu in May 2008.
- Delivered half a day session on ‘Microcontrollers in Robotics’ at Fr. Agnel College of Engineering Bandra in an ISTE sponsored Short Term Training Programme on ‘Robotics’
- Executed the order for supplying custom designed ‘Microcontroller Trainer Kits’ to ‘Mukesh Patel School of Technology and Management Engineering’ in December 2008. This order worth Rs 50,000/- being the first to be awarded to D. J. Sanghvi COE’s, consultancy development cell.
- Recipient of ‘Best Teacher Award’ for the academic year May 2008 – April 2009. Award constituted by SVKM management, includes a certificate of honor, and Rs 5,000/- in cash.
- Delivered a 3-hrs session, on ‘Application of Microcontrollers in Robotics’ with a demonstration session, at an ISTE sponsored STTP conducted at Fr. Conceicao Rodrigues COE, Bandra Mumbai, on 29<sup>th</sup> May, 2008.
- Convener of ISTE approved STTP on ‘Effective Communication and Life Skills’ conducted in the period 2<sup>nd</sup> June – 6<sup>th</sup> June 2008 at D. J. Sanghvi COE, VileParle (west).
- Delivered half-day session on PC based simulation for x51 using Keil, with practical sessions, at Fr. Conceicao Rodrigues COE, Bandra, Mumbai on 24<sup>th</sup> September 2008.
- Contributed for 2-days training in Keil – x51 cross compiler, and Orcad – PCB design and simulation, at ISTE recognized STTP conducted at Don Bosco Institute of Technology in November 2008.
- Delivered a 2-day session with fellow colleague on – Orcad as a PCB Design and Simulation tool, and Keil C-cross Compiler, at Don Bosco Institute of Technology, Kurla, Mumbai on 24<sup>th</sup> and 25<sup>th</sup> November 2008.

- Selected and Registered as a Doctoral candidate at Veermata Jijabai Technological Institute(VJTI), Matunga, Mumbai in December 2008.
- Delivered a 2-hrs session on ‘Practical Aspects in Orcad as a Simulation Tool’ at an ISTE approved STTP on ‘Simulation Tools and Languages’ at K. C. College of Engineering, Thane on 15<sup>th</sup> January 2009.
- Delivered lecture and demonstration session with hands-on session on ‘Keil – C cross Compiler’ for the students of third year EXTC K. J. Somaiya COE, Sion, Mumbai on 20<sup>th</sup> and 21<sup>st</sup> February 2009.
- Delivered an expert session for faculties at Don Bosco Institute of Technology, Kurla, Mumbai on ‘Microcontroller based Product Design’ organized by department of EXTC, on 21<sup>st</sup> March 2009.
- Delivered a 3-hour session for third year students at Thakur College of Eng. Kandivli (west). Topic - ‘Microcontrollers and Embedded Programming’ on 31<sup>st</sup> August 2009.
- Appointed as I/C Head – Department of Electronics Engineering in October 2009, and later confirmed as Head – Department of Electronics.
- October 2009-Invited as a panel judge for Narottam Sakhsaria Foundation national level scholarship program in Engineering.
- Invited to deliver an expert session for second year students at Thakur College of Eng. Kandivli (west). Topic - ‘Orcad- Layout and PCB Designing’ on 18<sup>th</sup> February 2010.
- February 2011 – Invited for one day expert session at Fr. Agnel Vashi for conducting a demo/interaction session for students of Third Year on the topic ‘Embedded Systems – Design, Debug and Testing Tools’
- October 2010-Invited as a panel judge for Narottam Sakhsaria Foundation national level scholarship program in Engineering.
- March 2011 - Delivered a One-day session at Fr. Agnel Bandra, at Short Term Training Program, in ‘Electronic Product Design’, on the topic ‘PCB – Design and Layout considerations’, with hands-on training to participants comprising of teacher and students.
- October 2011-Invited as a panel judge for Narottam Sakhsaria Foundation national level scholarship program in Engineering.
- November 2011 – Delivered a, 2-hrs, session (with demo) at Khalsa College Mumbai as a part orientation for the faculty members of Mumbai University BSc. - IT (Faculty), on the topic ‘Embedded Systems - Architectures and Overview’.

- January 2012 – Participated in Poster Presentation at V. J. T. I., Matunga, Mumbai on the topic ‘Radial Pulse Analysis’, won 2<sup>nd</sup> prize.
- February 2012 – Delivered a 2-hrs. session at Sardar Patel Institute of Technology (SPIT) Mumbai, on the topic ‘C for Embedded Development’, for the students of TE(Electronics)
- 18<sup>th</sup> and 19<sup>th</sup> May 2012 – Adviser to Interview Board for ‘Senior Radio Technician ’ post at GoI’s Staff Selection Commission (WR).
- 25<sup>th</sup> June – 4<sup>th</sup> July 2012 – Attended a 10-day workshop on ‘Research Methodology’ conducted by IIT- Bombay.
- 10<sup>th</sup>, 11<sup>th</sup> July 2012 – Invited as a guest speaker for Student Development Program, at Thakur College of Eng. Kandivli, on the topic ‘Embedded Systems Development’ , for the students of BE(Electronics, Extc., and Computers)
- October 2012 – Visiting NBA committee, audits the department and grants Accreditation for two years for the period April 2013 – March 2015.
- July 2013 – Delivered a 2-hrs. session on ‘Embedded Systems and Processors’, at Fr. C. Rodrigues Institute of Technology’, Vashi, at AICTE sponsored National level STTP on ‘VLSI and Embedded Systems’
- Recipient of ‘Best paper award’ and awarded the 1st prize at International Conference ‘Update Ayurveda - 2014’, organized by TNMC & BYL Nair Hospital, Mumbai, in December 2014..
- Awarded 1st prize for concept note presented at International Conclave on Health Tech. Innovations, organized by SAMEER, ICMR, NHSRC and DeitY at IIT – B, in January 2015.
- 13th, February 2015 – Delivered a 2-hrs. session entitled - ‘Skill Enhancement Lecture’, at Vivekananda Education Society’s Institute of Technology, Department of Electronics Engineering, on the topic ‘Microcontrollers and Embedded Systems’ for the second year students.
- 7th March, 2015 – Invited to deliver a session at one day national workshop on “Recent Trends in Embedded Systems”, organized by Department of Electronics and Telecommunications Engineering, Pillai HOC College of Engineering and Technology. Session topic ‘C for Embedded Architectures’
- July 2015 – Invited to conduct a two days teachers training program cum workshop on ‘AVR architecture’ and ‘Intel Galileo’ platform by

- department of Electronics and Telecommunication Engineering,  
Thakur College of Engineering and Technology, Kandivali, Mumbai.
- 3rd October, 2015 – Invited for session on ‘PC based hardware projects’, at inter collegiate one day seminar for diploma students ‘Pro – Search ‘15’, organized by Department of Comp. Sci. Eng., SBMP, VileParle(w).
  - 14th July – 16th July, 2016 and on 19th August 2016, successfully conducted workshop for students of Electronics and Biomedical Engineering (TE), on the open source PCB design and layout tool – KiCad.
  - 19th October 2016, successfully defended the thesis ‘Radial Pulse Analysis’, for Doctorate of Philosophy (Technology) in Electronics Engineering, from Veermata Jiabai Technological Institute under Mumbai University.

### **Publications :**

- Joshi P, Daruwala R (2013) Investigations into TIM perspective of Radial Pulse Analysis. Point-of-Care Healthcare Technologies (PHT), 2013 IEEE, p. 80-84 , Jan. 2013. doi : 10.1109/PHT.2013.6461289
- Joshi P, Kelkar P and Daruwala R (2013) Radial pulse analysis for circadian cycle evaluation. TENCON 2013 - 2013 IEEE Region 10 Conference (31194) , p.1-4, Oct. 2013. doi :10.1109/TENCON.2013.6719 034
- Joshi P, Daruwala R, (2013) Objective evaluation of radial pulse signal. India Conference (INDICON), 2013 Annual IEEE, pp.1-4, 13-15 Dec. 2013. doi:10.1109/INDCON.2013.6726081
- Joshi P, Daruwala R, (2014) Exploring Multiscale Entropy Measures for Radial Pulse Analysis with Traditional Indian Medicine (TIM) perspective for dominant humor identification. Update Ayurveda 2014, 11-13 Dec. 2014, TNMC & BYL Nair Ch. Hospital, Mumbai, INDIA.

**Above paper was adjudged as the best paper in prize paper presentation category and awarded the 1st prize.**

- Joshi P, Daruwala R, (2015) Investigation of Radial Pulse Analysis with Traditional Indian Medicine(TIM) perspective for diagnosis. HealthTech Innovations 2015 - Proceedings, pp. 51-52, 9-10 Jan. 2015, SAMEER, IIT – Bombay, INDIA.  
**Above concept note was awarded ‘Best Concept Note’, prize in the theme: Diagnosis and treatment of diseased by Traditional Methods.**
- Joshi P, Ambekar K, Daruwala R (2015) Traditional Indian Medicine (TIM) based Humor Identification - A recurrence based Approach. International Journal of Computer Applications 113(14):33-37, March 2015. doi: 10.5120/19898-1996
- Joshi P. and Daruwala R. (2016) Exploring Radial Pulse Analysis using Multi-Scale Entropy Measures for Pathologic Condition Indication, IEEE INDICON 2016, IISc, Bengaluru, Dec – 2016, accepted for oral presentation.