



**SHRI VILE PARLE KELAVANI MANDAL'S
DWARKADAS J. SANGHVI COLLEGE OF ENGINEERING
RE-EXAMINATION TIMETABLE - June 2020**

(conducted in October 2020 due to Covid-19)

THIRD YEAR, BACHELOR OF ENGINEERING, SEMESTER-V

(Prior to 2019-20 Batch Students)

Timings : 08:00 a.m. to 09:00 a.m.(Online Mode)



Date : 29/09/2020

Day	Date	ELECTRONICS ENGINEERING
Wednesday	30/9/2020	Design with Linear Integrated Circuits
Saturday	17/10/2020	Digital Communication
Monday	19/10/2020	Signals and Systems

Important Note:


1. Changes if any, in the time table, shall be communicated on the College website(djsce.ac.in).

SD

Signature:

PRINCIPAL

Signature:


29/09/2020